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Electronics and

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Engineering | Fifth
Semester Beginning

Electrical

Measurement

/u0026

Instrumentation

Lecture # 1

MOST EXPECTED

VIVA VOICE

QUESTIONS FOR

BASIC ELECTRONICS

LAB PART 1 Sampling

Theorem (DSP Lab) |

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~~V Sem | ECE | EXP1 |~~
~~S1~~

Expirimet 5 CN Lab

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Part - 2 | Lab Viva |

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components | How to

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VIVA QUESTIONS

AND ANSWERS

18ECL58- HDL LAB -

1 Common

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Electronics Why and
How to use capacitor
| Basic electronics
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~~Transistors, How do
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Oscilloscope Speed

Tour of My

Electronics Book

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Interview Questions

And Answers

Electronic

Engineering Job

Interview Questions

(Part 1) ~~AC vs DC~~

~~Explained and How to~~

~~Use an Oscilloscope~~

MAKAUT LAB/PRAC

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A Viva QUE /u0026

ANS EVEN SEM

EXAM VIVA

QUESTIONS ON PN

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AND TRANSISTOR ||

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#ApniPryogShala

#PNJunction Top 50

Digital Signal

Processing ece

technical interview

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answers tutorial for

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Quiz | Basic Features |
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Period 2 Thevenin's
Theorem Explained
(With examples of
Independent and
Dependent Sources)
Basic Electronics
introduction for
technical interviews

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CBCS Scheme

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2

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and Computer

Engineering - Fort

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IN. \$8-\$17 Per Hour

(Glassdoor est.) 24h.

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both ECE department
and SEM lab with
regular office hours
8-12 pm (ECE) and
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prepare well for your

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are very important,

these questions may

be asked in your

interview too & in

your technical round

also.

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viva questions. At the
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B.E. FIRST YEAR

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CHEMISTRY LAB

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VOLUMETRIC

ANALYSIS 1. Why hot
liquids should not be
taken in the Burette?

A. Hot liquids should

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Viva Question
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not be placed in the burette because the instrument is calibrated at a much lower temperature (15 to 20°C). 2. Which meniscus is read in case of colored solution taken in a burette? A.

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ENGINEERING
CHEMISTRY LAB

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VIVA-VOCE

VLSI Lab Manual VII
sem, ECE 10ECL77

_____ GCEM 7

5. LIST OF
EXPERIMENTS EXPT.
NO. Name of the
Experiment PAGE NO.
Part A : Digital design
8 01 Inverter 10 02
Buffer 11 03
Transmission gate 13
04 a) Basic gates 15
b) Universal gates 18

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05 a) D Flip-flop 20

Answer

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weeks of industrial
training related to
ECE will be held after
6th semester;
however, viva-voce
will be conducted in
7th Semester (ETEC
461).

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Important:-Elective
Paper will be offered
in 7th Semester, if at-
least one-third of the
total students opt for
the same.

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of ...

Paving the way for
fifth-generation (5G)
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communication, developing powerful and sophisticated medical diagnostic tools, improving the trustworthiness of microchips to keep computer hardware safe from cyber-attackers, exploring the outer limits of nanoelectronics — NYU researchers are doing all that and

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classroom of students from different parts of the world, distinguished professors working on advanced research projects, giving you a chance to take part, gradually teaching you core professional values, forms a winning combination."

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Embedded Systems

Design; 4) A grade of

at least C- is required

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in CS-UY 1114 or CS-UY 1133, CS-UY 2204, ECE-UY 2004 and ECE-UY 3054. 5) CS-UY 1114 is strongly recommended, but CS-UY 1133 is also acceptable (for students changing major to ECE, etc.).

Electrical
Engineering, B.S. |

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B.TECH VII

SEMESTER

COMMSIM LAB

ECE-419. L T P Time -

3 Hrs - - 3 Sessional-

60 Prac./Viva- 40

Credit- 1.5. LIST OF

EXPERIMENTS: 1. To

Study and simulate

transmission and

reception of AM. 2.

To Study and

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simulate transmission
and reception of FM.

3. To Study and
simulate transmission
and reception of PM.
- 4.

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Notes: EC8501 Notes

Digital
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EC8553 Notes

Discrete Time Signal
Processing. EC8552

Notes Computer
Architecture and

Organization. EC8551
Notes Communication

Networks. Semester 6
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